

FIG. 2

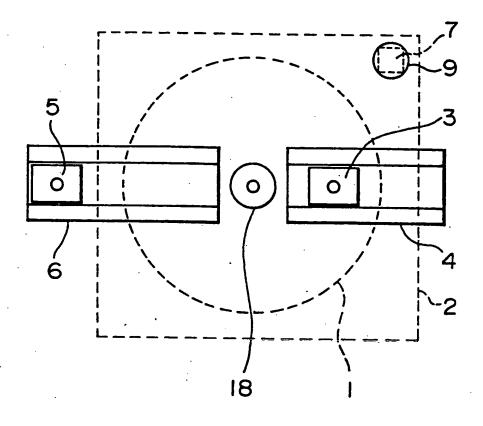


FIG. 3A

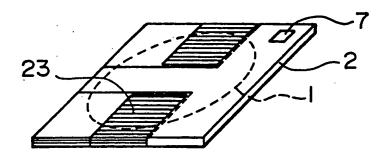
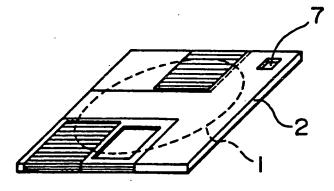


FIG. 3B





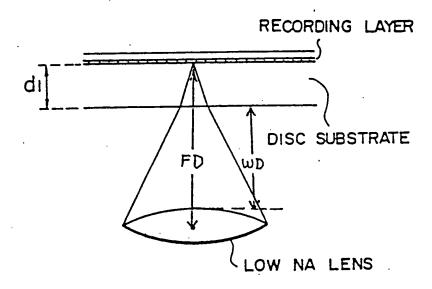
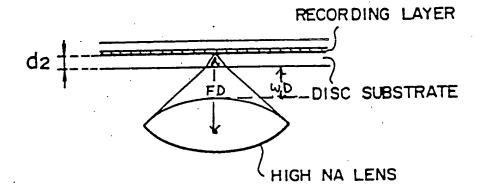
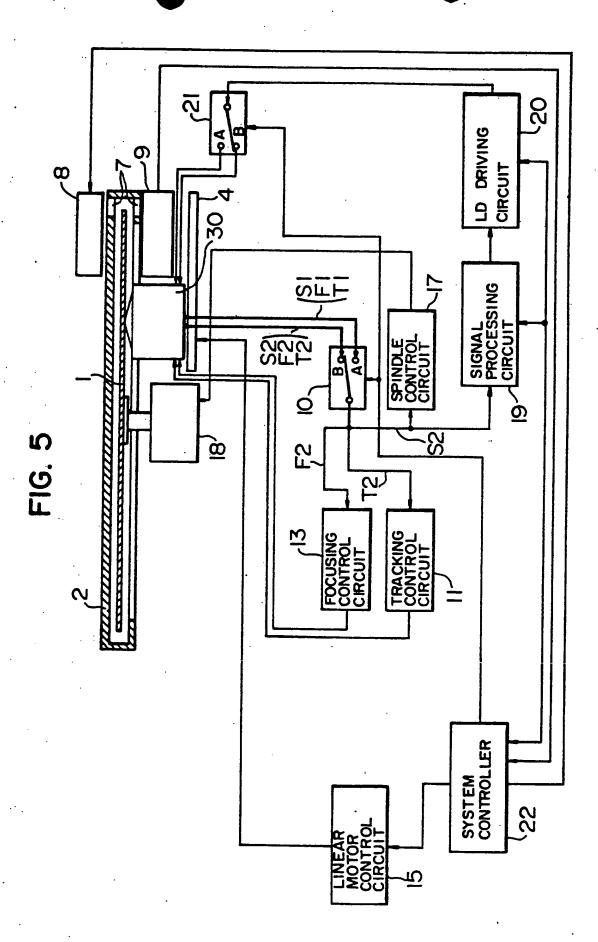


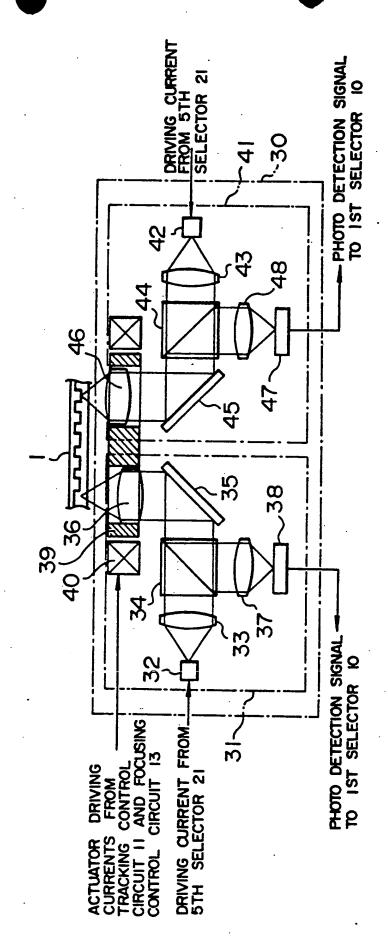
FIG. 4B





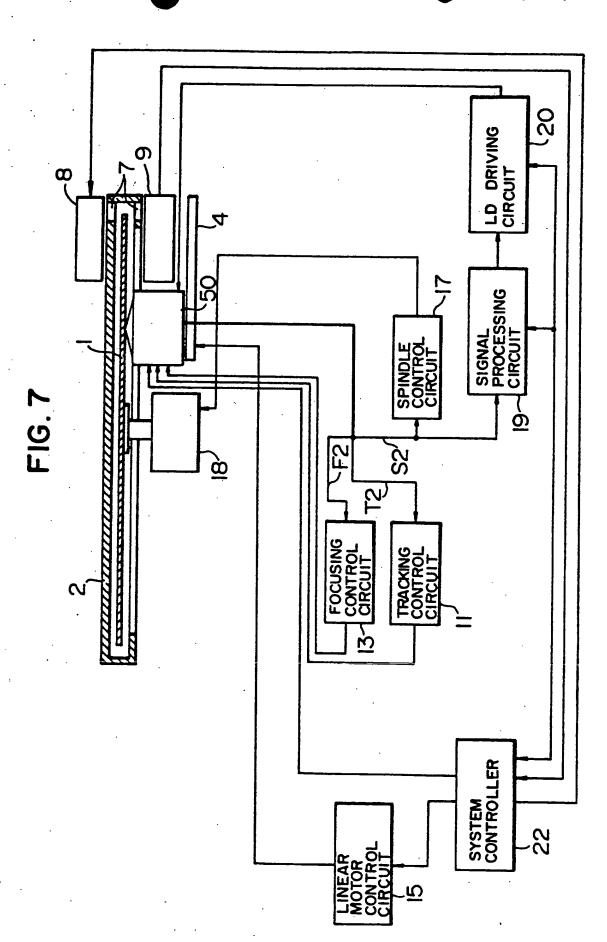
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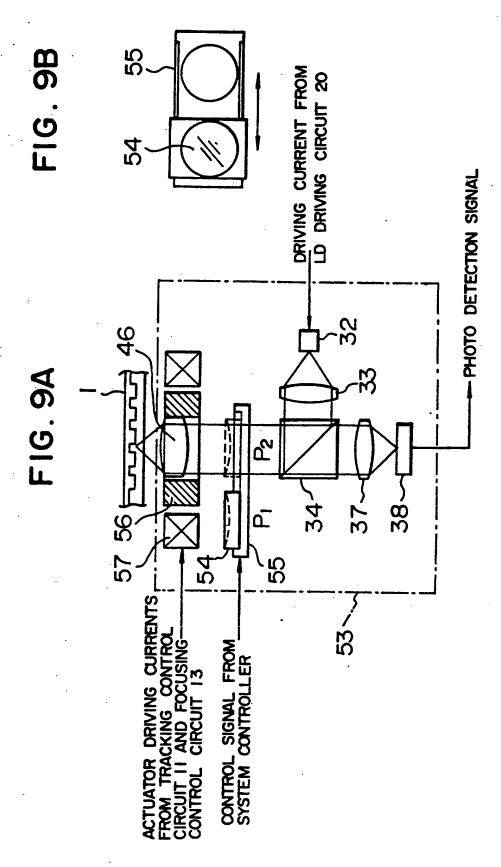
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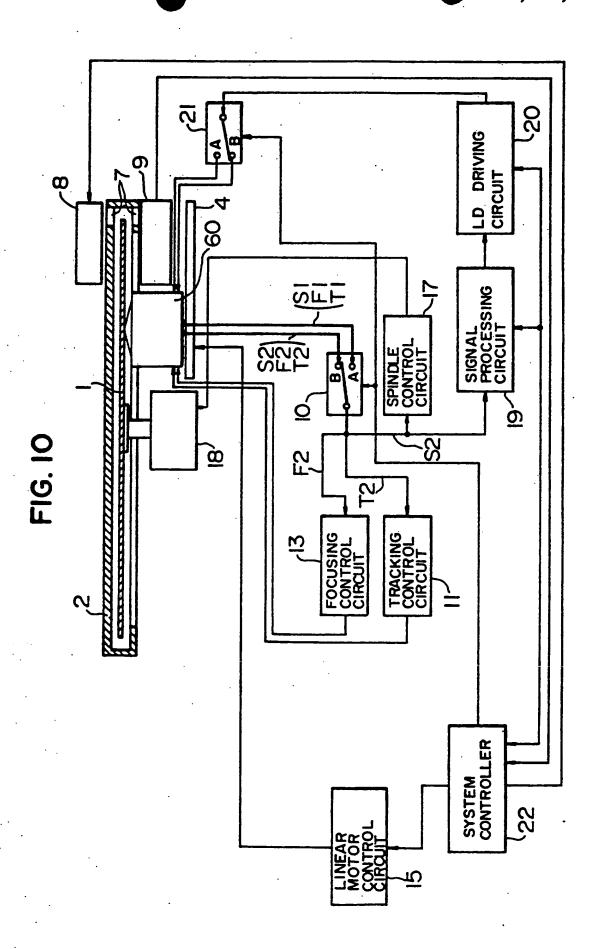


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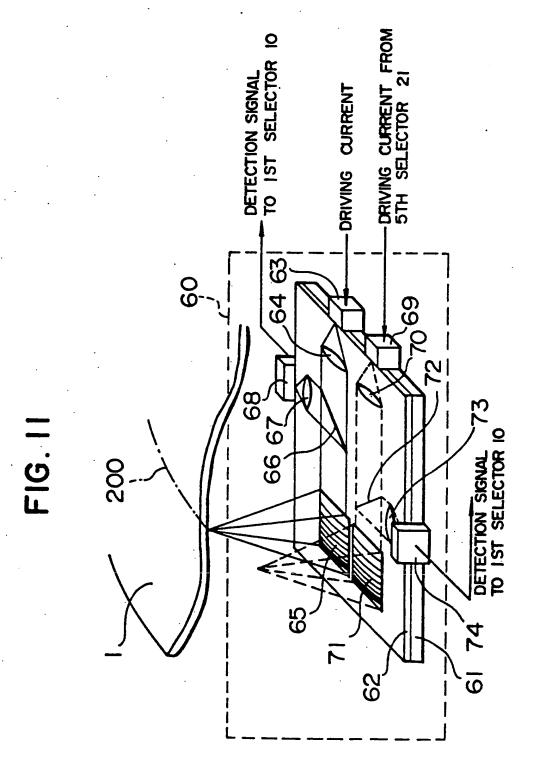
DRIVING CURRENT FROM LD DRIVING CIRCUIT 20 -52 39 5 CONTROL SYSTEM ( CONTROL











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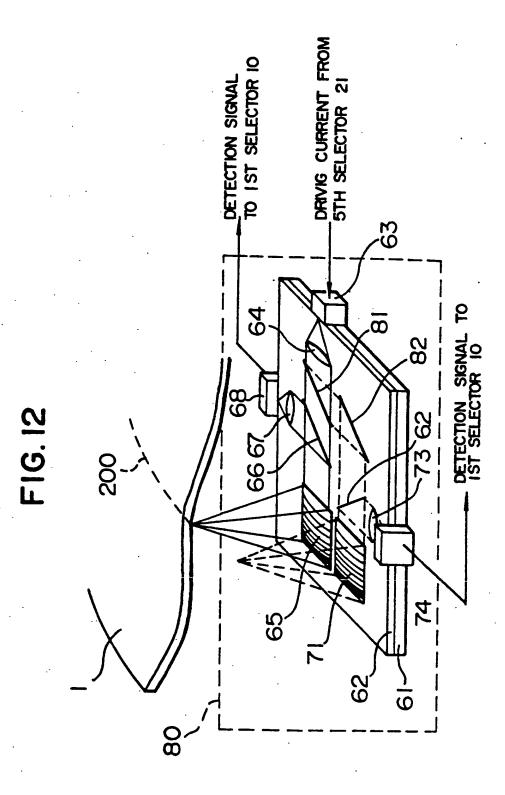
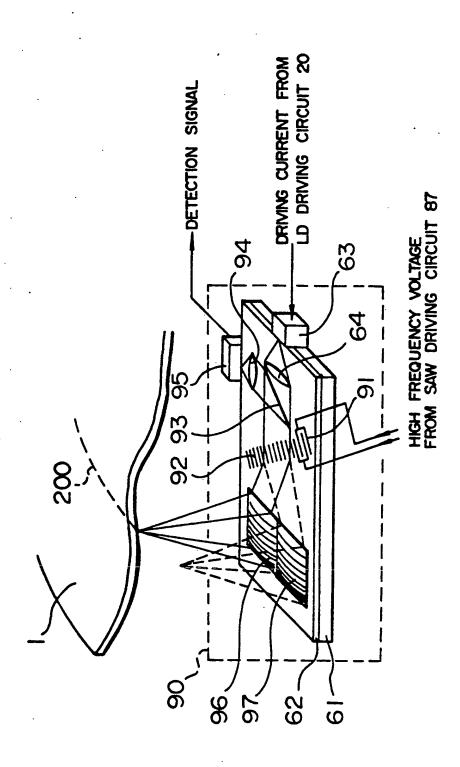
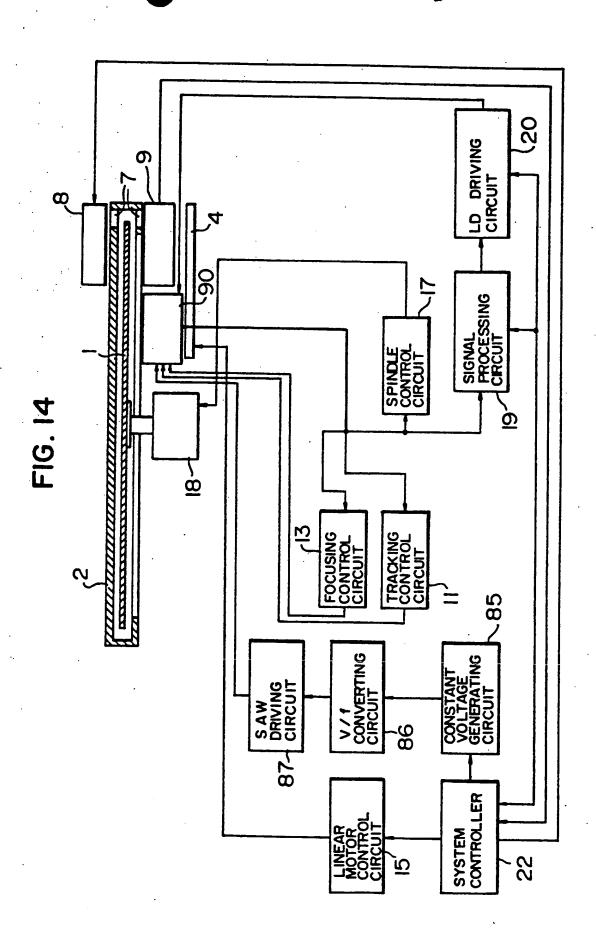


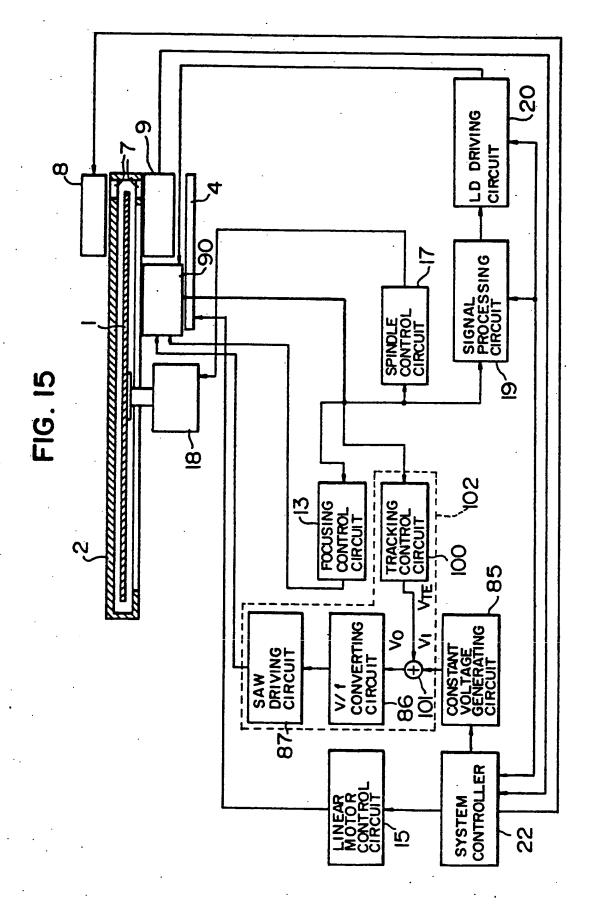
FIG. 13

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FIG. 16

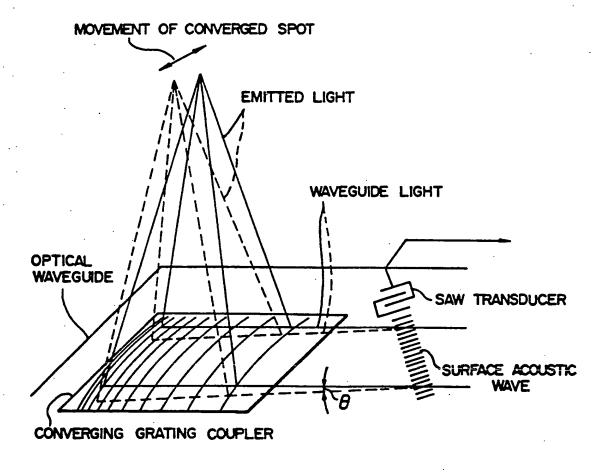
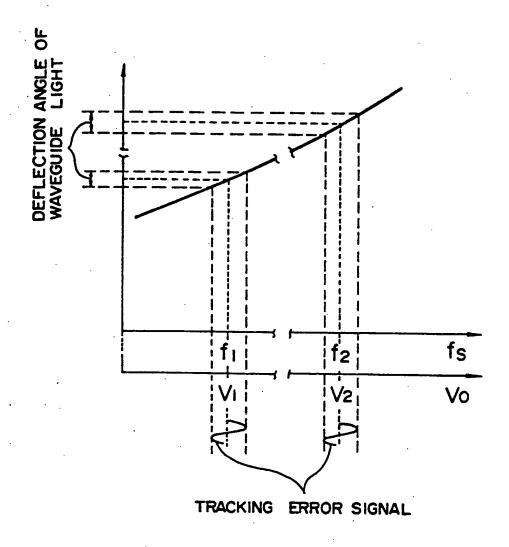
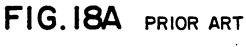


FIG. 17



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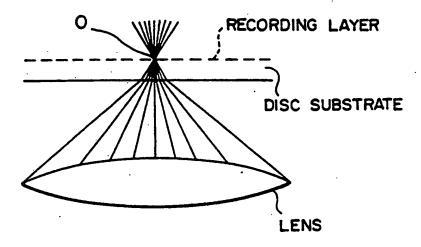


FIG. 18B PRIOR ART

